	Hits	Search Text	DBs	Time Stamp
1	1	6182233.pn.	USPAT	2004/03/25 08:35
2	377	reduc$3 near3 latches	USPAT	2004/03/19 13:12
3	116	reduc$3 near3 latches with number	USPAT	2004/03/19 13:58
4	0	reduc$3 near3 latches with number and (lactch near4 logic)	USPAT	2004/03/19 13:23
5	0	reduc$3 near3 latches with number and (lactch near4 circuit)	USPAT	2004/03/19 13:23
6	8147	switch near5 latch	USPAT	2004/03/19 13:59
7	5598	switch with latch with (logic circuit)	USPAT	2004/03/19 13:59
8	14	switch with latch with (logic circuit) with (connect and disconnect)	USPAT	2004/03/19 14:19
9	1	switch with latch with (logic circuit) with (connect and disconnect)	US-PGPUB; EPO; JPO; IBM_TDB	2004/03/19 14:03
10	719	enabl$3 near2 latch$2 near3 (receiv$3 transmit$3)	USPAT	2004/03/19 14:08
11	17	enabl$3 near2 latch$2 near3 (receiv$3 transmit$3) with only	USPAT	2004/03/22 13:48
12	3	enabl$3 near2 latch$2 near3 (receiv$3 transmit$3) with only	US-PGPUB; EPO; JPO; IBM_TDB	2004/03/19 14:13
13	2	("5093588" "5128557").pn.	USPAT	2004/03/19 14:19
14	25	switch near2 connects with (logic circuit) with latch	USPAT	2004/03/19 14:36
15	8	switch near2 connects with (logic circuit) with latch	US-PGPUB; EPO; JPO; IBM_TDB	2004/03/19 14:33
16	5	latch with clocks with only with operation	USPAT	2004/03/19 14:43
17	7	latch with clocks with only with operation	US-PGPUB; EPO; JPO; IBM_TDB	2004/03/19 14:39
18	0	one adj1 stage with 'at a time'	USPAT	2004/03/19 14:46
19	0	one adj1 stage with 'at a time'	US-PGPUB; EPO; JPO; IBM_TDB	2004/03/19 14:46
20	1	interlocking adj1 handshake	USPAT	2004/03/19 14:56
21	1	interlocking adj1 handshake	US-PGPUB; EPO; JPO; IBM_TDB	2004/03/19 14:56

	Hits	Search Text	DBs	Time Stamp
22	2	IPCMOS	USPAT	2004/03/19 17:21
23	55	micropipelines	USPAT	2004/03/19 17:19
24	5	micropipelines and (switch with latch)	USPAT	2004/03/19 17:19
25	1	micropipelines and (switch with latch)	US-PGPUB; EPO; JPO; IBM_TDB	2004/03/19 17:19
26	2	micropipelines	EPO; JPO; IBM_TDB	2004/03/19 17:19
27	0	IPCMOS	EPO; JPO; IBM_TDB	2004/03/19 17:21
28	0	(first adj1 circuit) same (second adj1 circuit) same (latch register) same (local adj1 clock) same switch	USPAT	2004/03/22 13:19
29	2	(first adj1 circuit) same (second adj1 circuit) same (latch register) same (local adj1 clock)	USPAT	2004/03/22 13:28
30	0	(first adj1 circuit) same (second adj1 circuit) same (latch register) same (local adj1 clock)	US-PGPUB; EPO; JPO; IBM_TDB	2004/03/22 13:28
31	283	enabl$3 near2 latch$2 near5 only	USPAT	2004/03/22 13:48
32	17	enabl$3 near2 latch$2 near5 only with (operation valid)	USPAT	2004/03/22 14:47
33	33	enabl$3 near2 latch$2 near5 only and (power adj1 (consumption reduc$4 conserv$5 sav$3))	USPAT	2004/03/22 14:46
34	0	enabl$3 near2 latch$2 near5 only and (power adj1 (consumption reduc$4 conserv$5 sav$3))	EPO; JPO; IBM_TDB	2004/03/22 14:46
35	1	enabl$3 near2 latch$2 near5 only with (operation valid)	EPO; JPO; IBM_TDB	2004/03/22 14:47
36	65030	(coupl$3 near3 input) near5 output	USPAT	2004/03/24 12:56
37	5777	(coupl$3 near3 input) near5 output with switch	USPAT	2004/03/24 12:56
38	472	(coupl$3 near3 input) near5 output with switch with selectively	USPAT	2004/03/24 13:06
39	17	(coupl$3 near3 input) near5 output with switch with selectively with stage	USPAT	2004/03/24 13:54
40	1	(coupl$3 near3 input) near5 output with switch with selectively with stage	EPO; JPO; IBM_TDB	2004/03/24 13:04
41	25	(coupl$3 near3 input) near5 output with (local adj1 clock)	USPAT	2004/03/24 13:51

	Hits	Search Text	DBs	Time Stamp
42	3	(coupl$3 near3 input) near5 output with (local adj1 clock)	US-PGPUB; EPO; JPO; IBM_TDB	2004/03/24 13:51
43	32	(acknowledge adj1 signal) with (valid adj1 signal)	USPAT	2004/03/24 14:07
44	1	(acknowledge adj1 signal) with (valid adj1 signal)	EPO; JPO; IBM_TDB	2004/03/24 14:07
45	7382	request near10 acknowledg$5	USPAT	2004/03/24 14:08
46	415	(request near10 acknowledg$5) with (neighbour$3 adjacent next)	USPAT	2004/03/24 14:11
47	25	(request near10 acknowledg$5) with (neighbour$3 adjacent next) with clock	USPAT	2004/03/24 14:22
48	2	(request near10 acknowledg$5) with (neighbour$3 adjacent next) with clock	EPO; JPO; IBM_TDB	2004/03/24 14:17
49	0	interlock$3 near3 (clock adj1 circuits)	USPAT	2004/03/24 14:23
50	0	interlock$3 near3 (clock adj1 circuits)	EPO; JPO; IBM_TDB	2004/03/24 14:23
51	64	(enable disable) near3 ((adjacent neighbour$3 next) adj1 stage)	USPAT	2004/03/24 14:39
52	34	(enable disable) near3 ((adjacent neighbour$3 next) adj1 stage) and ((switch coupl$3) with (latch stage circuit))	USPAT	2004/03/24 14:37
53	1	(enable disable) near3 ((adjacent neighbour$3 next) adj1 stage) and ((switch coupl$3) with (latch stage circuit))	EPO; JPO; IBM_TDB	2004/03/24 14:37
54	1120	scan adj1 chain	USPAT	2004/03/24 14:39
55	0	scan adj1 chain with stage and (enabl$3 near2 switch$2)	USPAT	2004/03/24 14:42
56	47	scan adj1 chain with stage	USPAT	2004/03/24 14:41
57	4	scan adj1 chain with stage and (enabl$3 near2 clock)	USPAT	2004/03/24 14:53
58	1749	enabling with only with operation	USPAT	2004/03/24 14:53
59	0	enabling with only with operation with (locak adj1 clock)	USPAT	2004/03/24 14:53
60	68	enabling with only with operation with clock	USPAT	2004/03/24 15:34
61	1	(enabl$3 near2 stage) with (disabl$3 near2 ((adjacent neighbour$3 next) adj1 stage))	USPAT	2004/03/24 15:36
62	82	enabl$3 near2 (local adj1 clock$1)	USPAT	2004/03/24 15:36

	Hits	Search Text	DBs	Time Stamp
63	1	enabl$3 near2 (local adj1 clock$1) with only	USPAT	2004/03/24 15:43
64	1	handshake near3 (local adj1 clock)	USPAT	2004/03/24 15:50
65	100	handshake near3 clock$1	USPAT	2004/03/24 15:52
66	16	handshake near3 clock$1 and (power near1 (consum$5 reduc$4 sav$3 conserv$4))	USPAT	2004/03/24 16:23
67	4	5920899.URPN.	USPAT	2004/03/24 16:07
68	7	handshake near3 clock$1 and (power near1 (consum$5 reduc$4 sav$3 conserv$4))	US-PGPUB; EPO; JPO; IBM_TDB	2004/03/24 16:22
69	0	logic with latch with switch$1 with (global adj1 clock)	USPAT	2004/03/24 16:23
70	8	logic same latch same switch$1 same (global adj1 clock)	USPAT	2004/03/24 16:32
71	158	(consum$5 near2 power) near3 only near5 (operation perform latch$3)	USPAT	2004/03/24 16:34
72	122	(consum$5 near2 power) near3 only near5 operation	USPAT	2004/03/24 16:57
73	0	(consum$5 near2 power) near3 only and micropipeline	USPAT	2004/03/24 16:58
74	66	(consum$5 near2 power) near3 only and (request with acknowledg$5)	USPAT	2004/03/24 17:04
75	33	(consum$5 near2 power) near3 only and (request with acknowledg$5) and (switch near5 (stage circuit))	USPAT	2004/03/24 17:11
76	1399	latch near3 interlock$3	USPAT	2004/03/24 17:11
77	0	latch near3 interlock$3 near2 pipelined	USPAT	2004/03/24 17:12
78	0	latch near3 interlock$3 near2 cmos	USPAT	2004/03/24 17:12
79	3	latch near3 interlock$3 and ((logic adj1 circuit) near3 latch)	USPAT	2004/03/24 17:13
80	138	(connect$3 disconnect$3) near3 (logic circuit) same (local adj1 clock)	USPAT	2004/03/25 08:37
81	11	(connect$3 disconnect$3) near3 (logic circuit) same (local adj1 clock) same latch	USPAT	2004/03/25 08:45
82	20	(latch$3 near2 data) near5 (local adj1 clock)	USPAT	2004/03/25 08:54